

FIG.1
Related Art

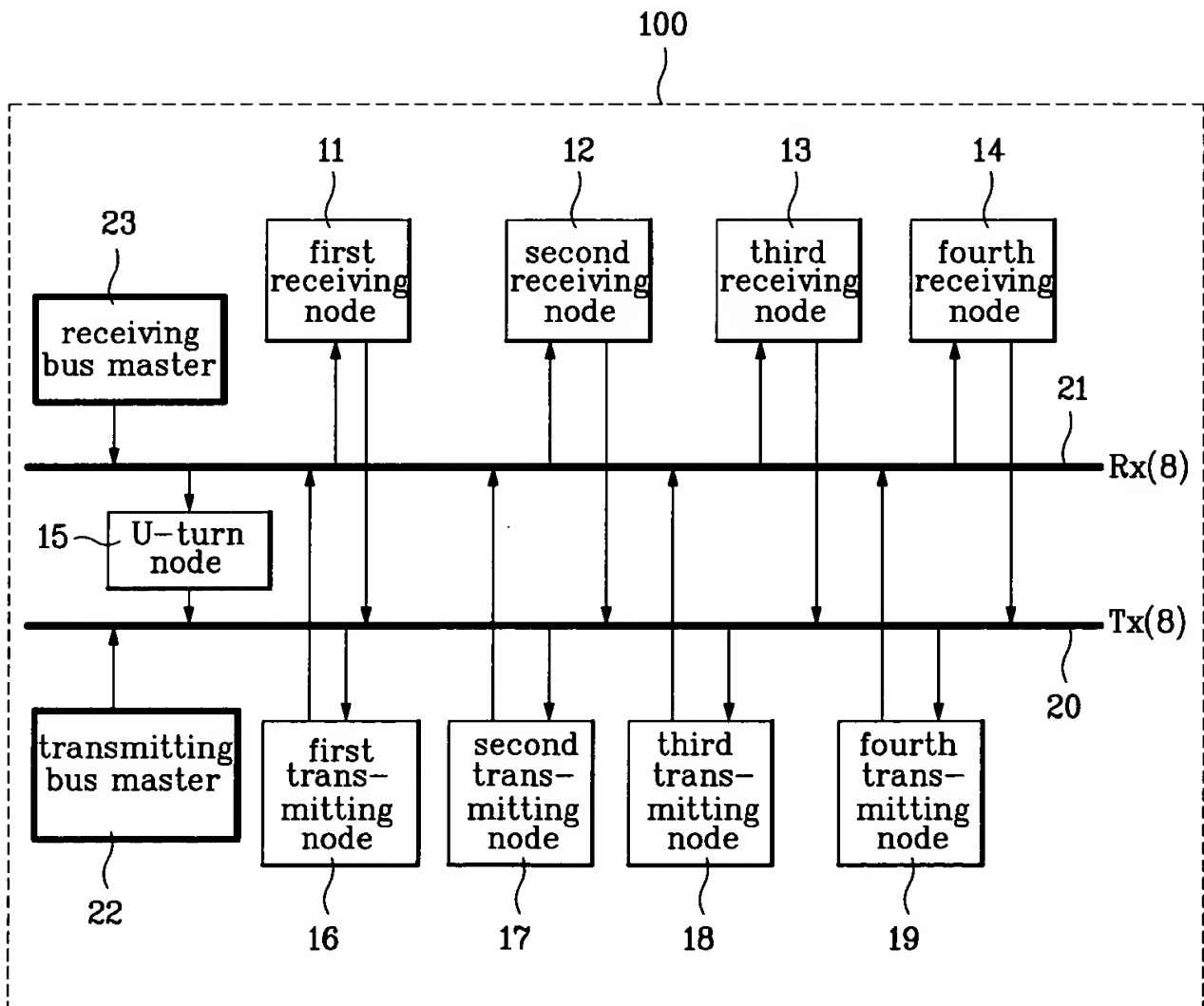


FIG.2
Related Art

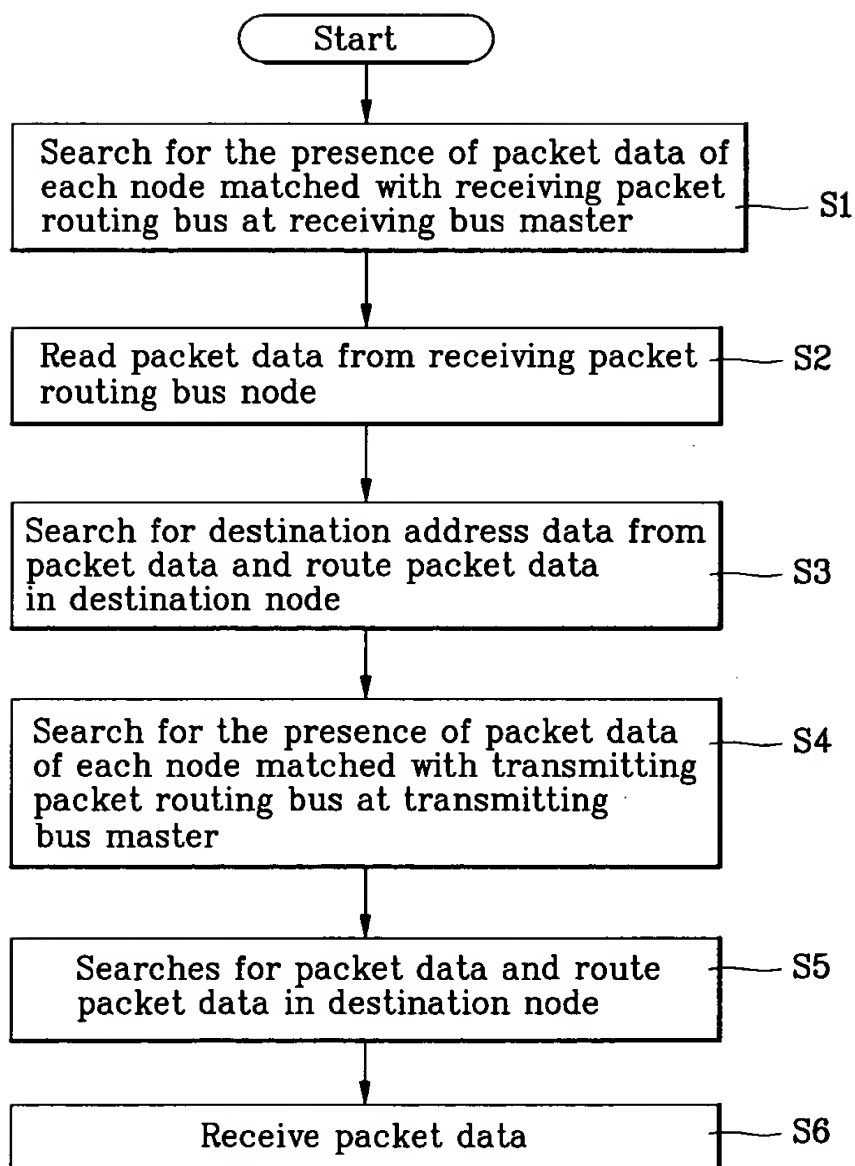


FIG. 3
Related Art

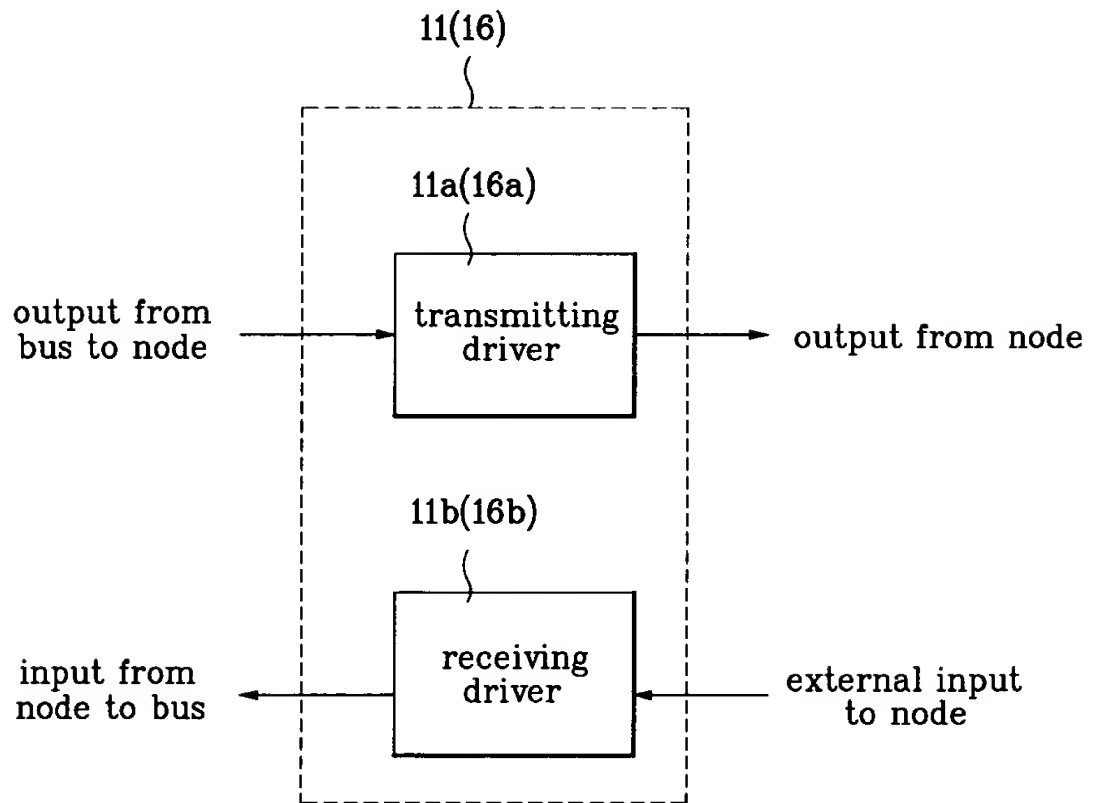


FIG. 4

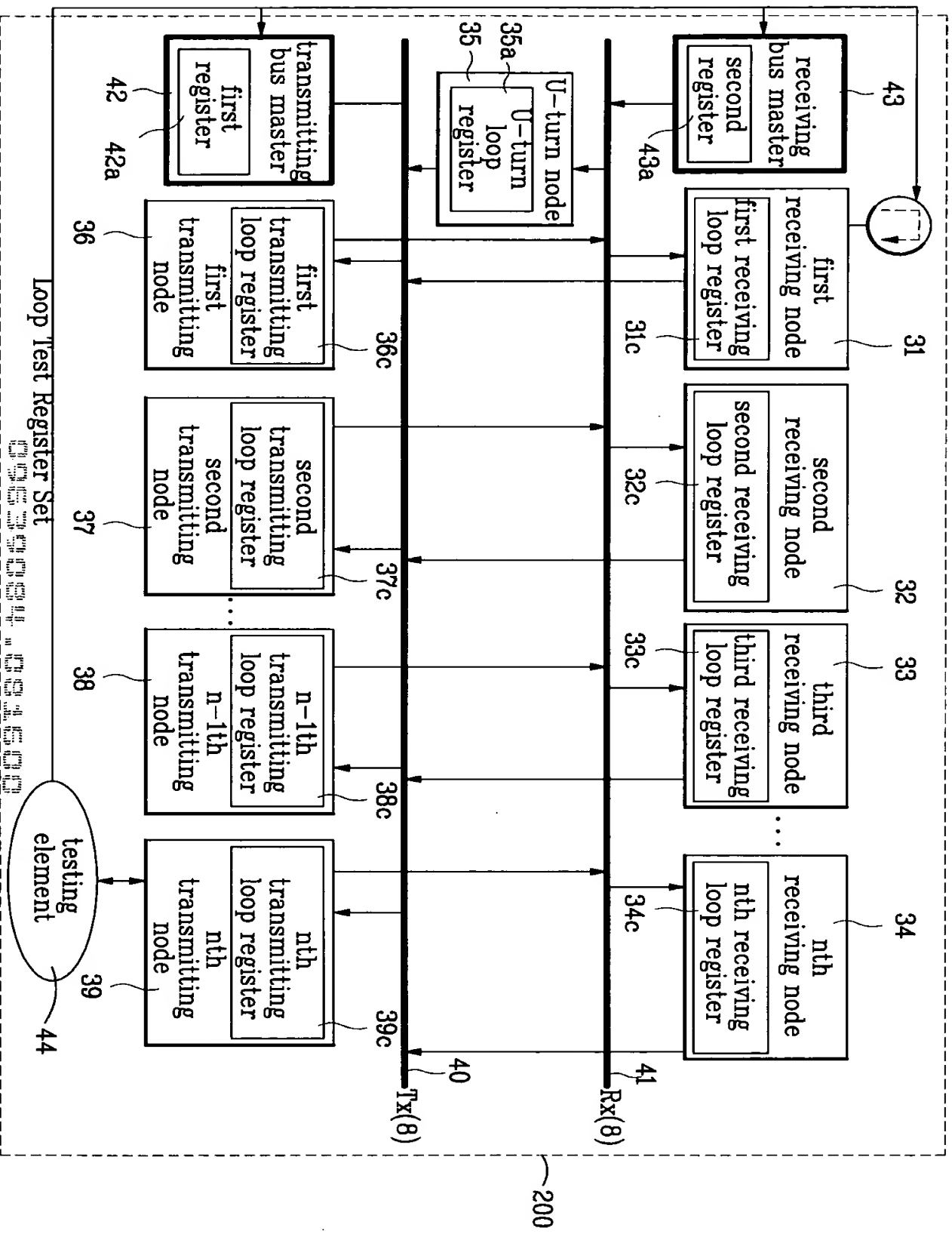


FIG. 5

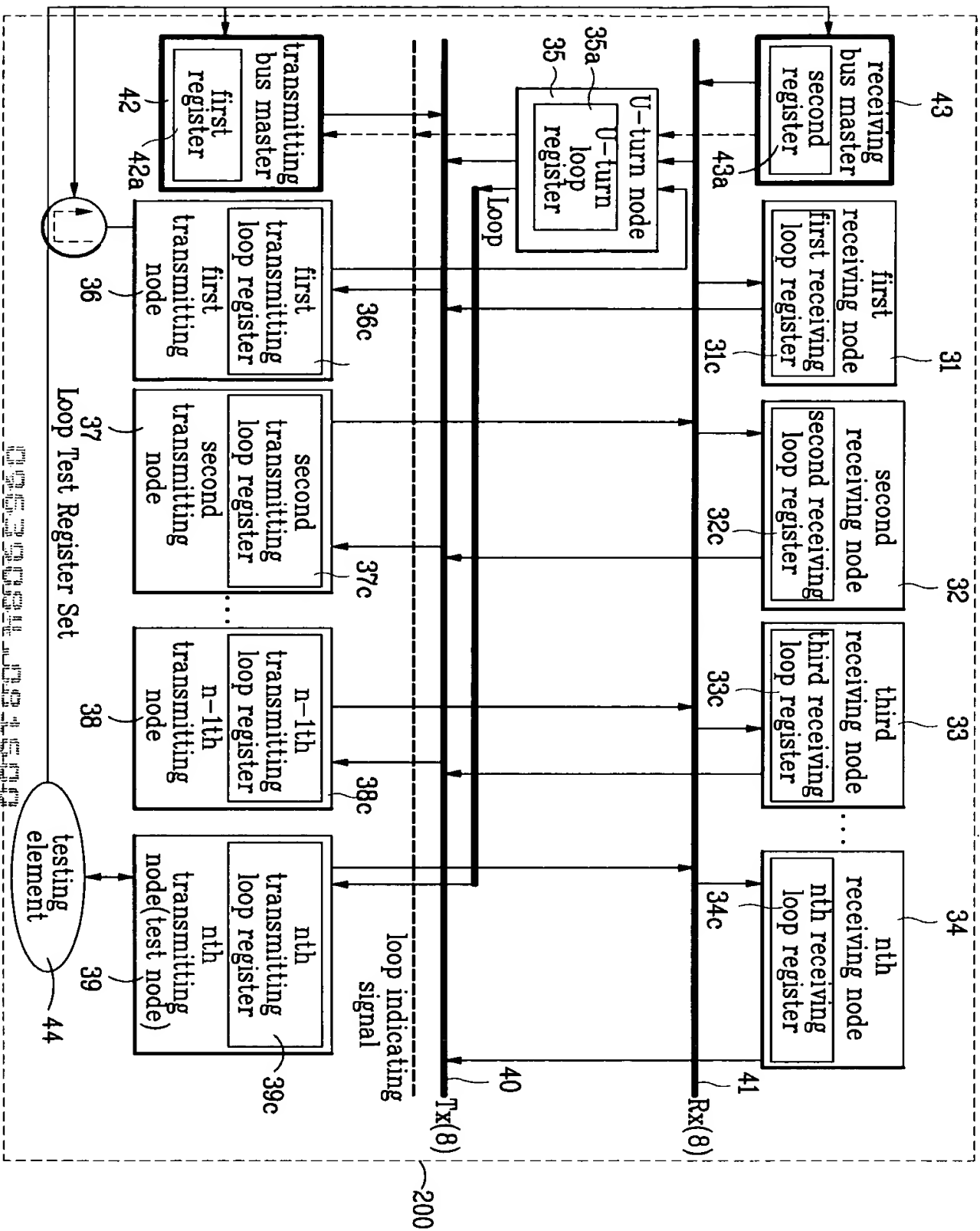
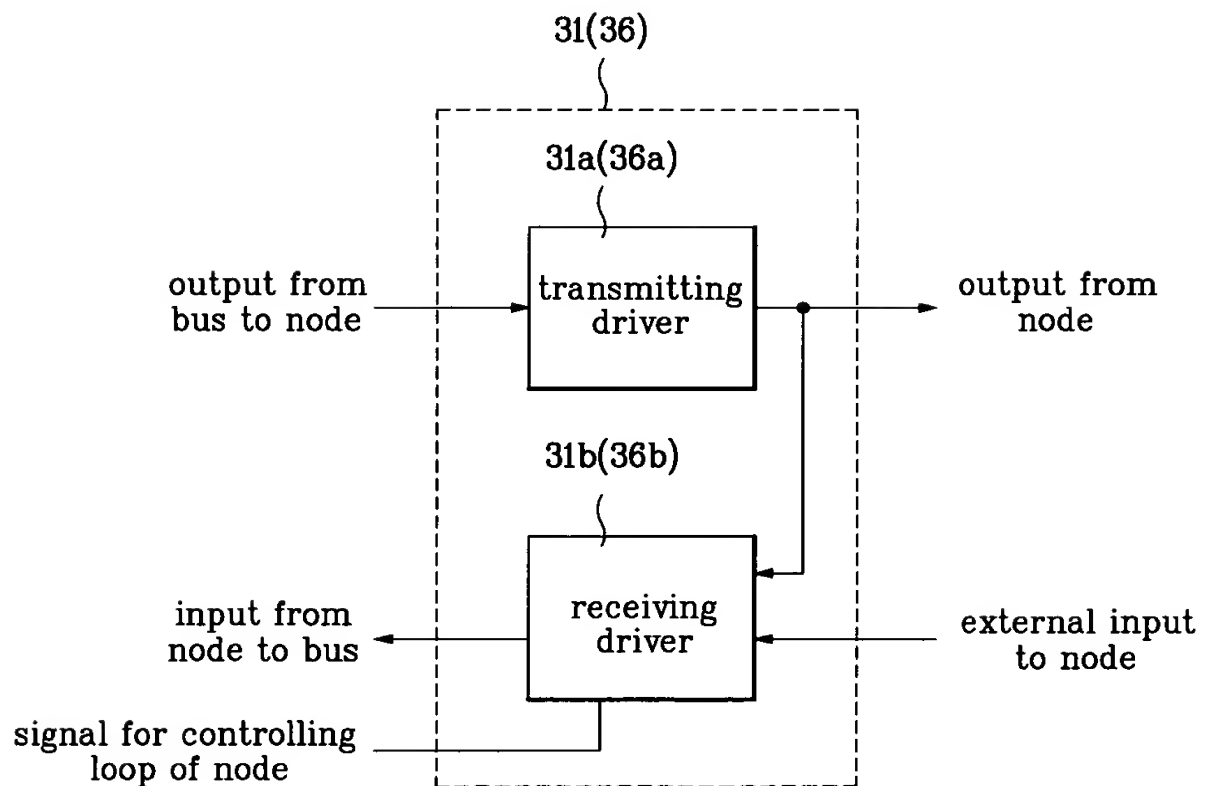


FIG.6



value0 : external input to node
value1 : output loop signal

FIG. 7

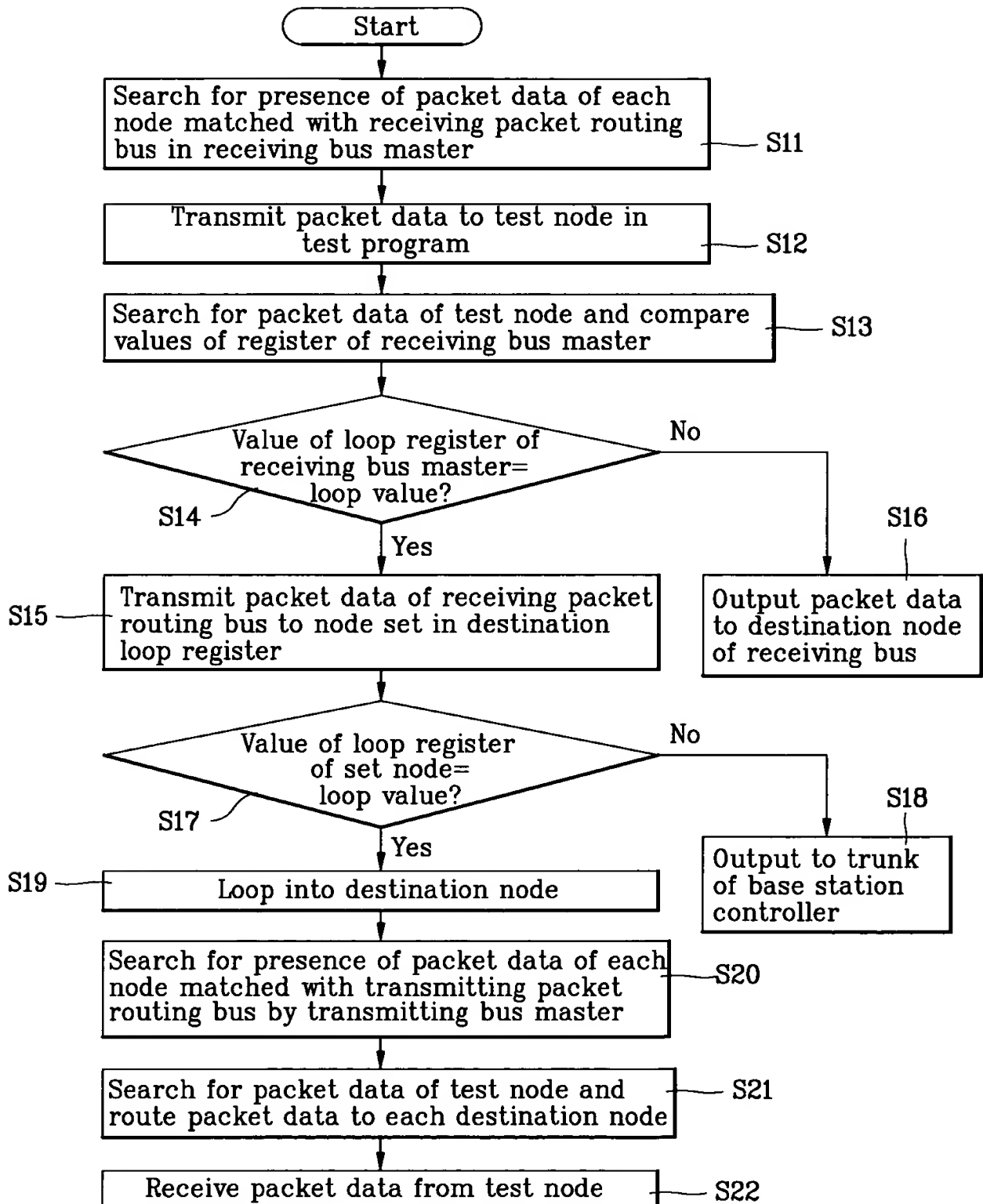


FIG. 8

